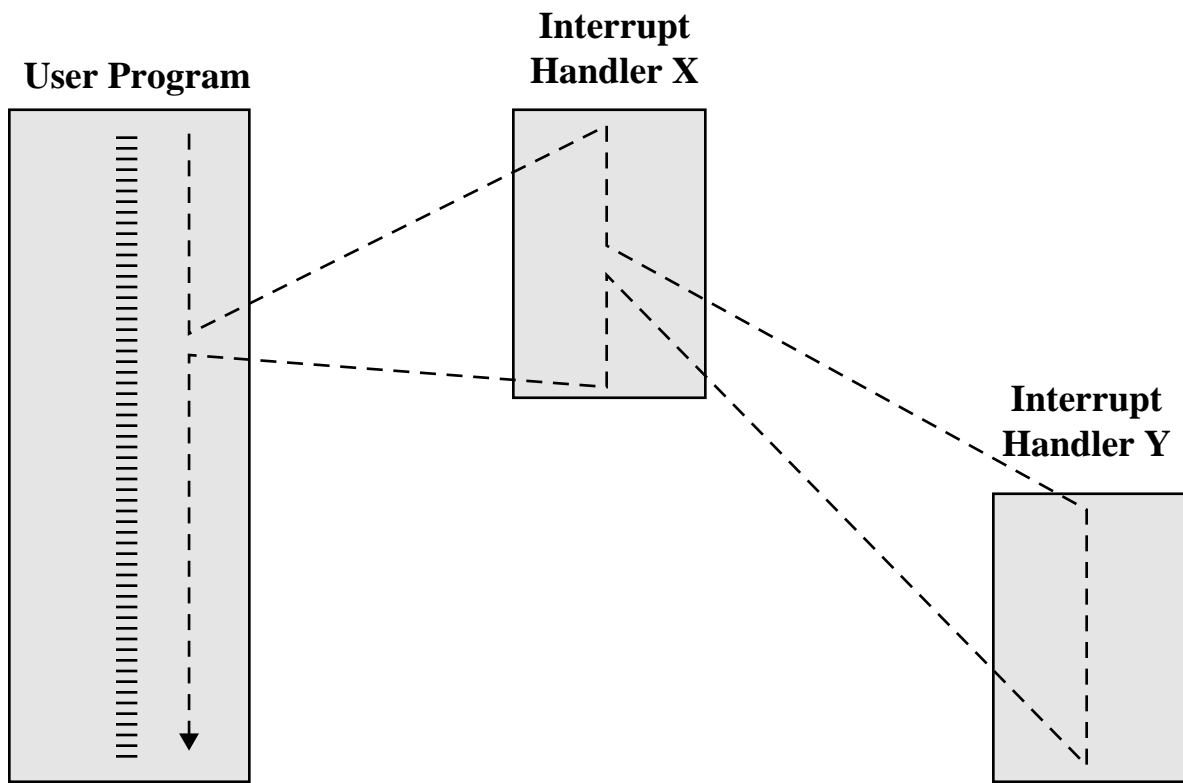


(a) Sequential interrupt processing



(b) Nested interrupt processing

**Figure 3.13 Transfer of Control with Multiple Interrupts**