(a) Traditional Bus Architecture
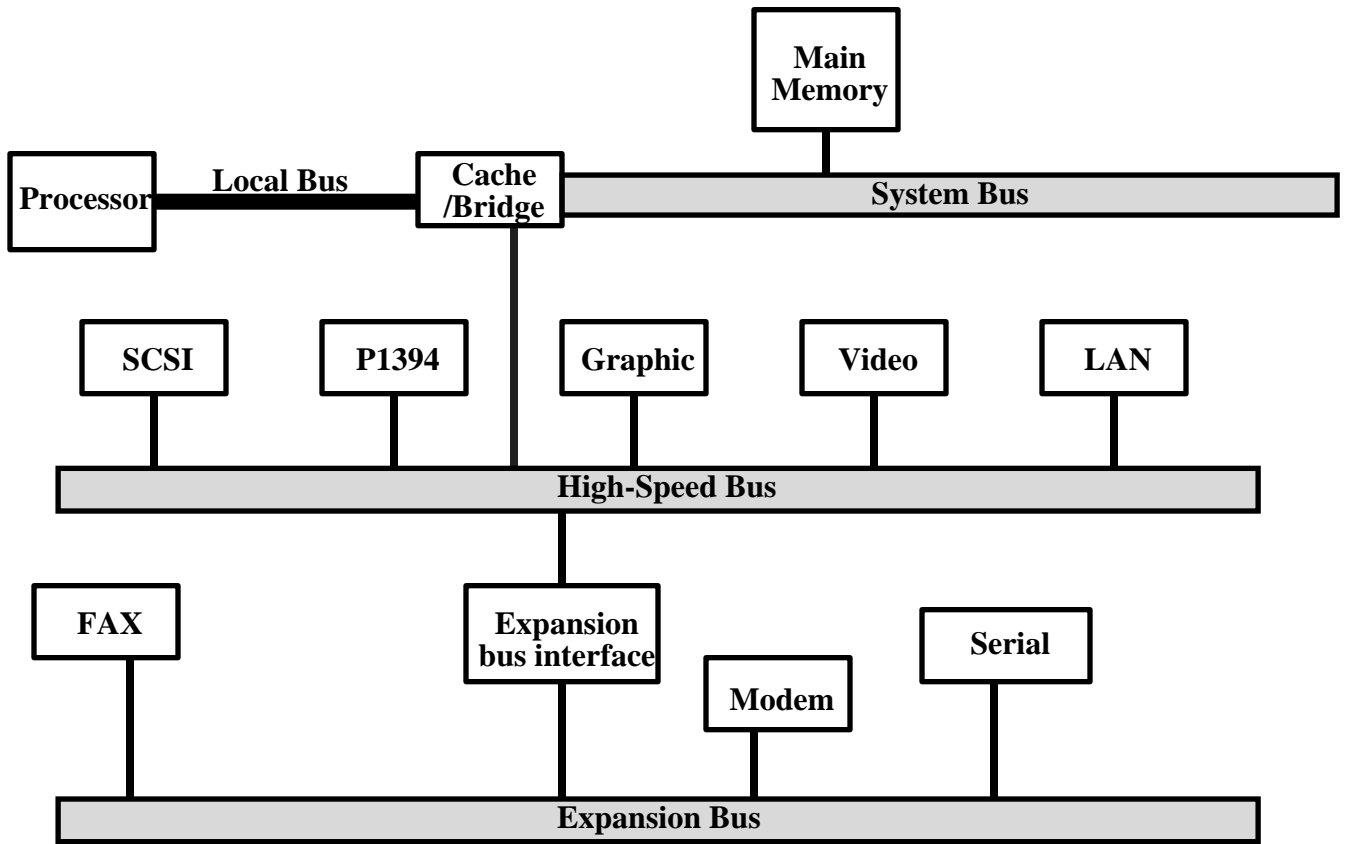

(b) High-Performance Architecture

Figure 3.18 Example Bus Configurations